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Inventor

Tatsuya SUZUKI

Serial No.

09/823,058

Filed

March 30, 2001

Title

STOCK/TRANSFER VESSEL FOR

SEMICONDUCTOR SUBSTRATE AND METHOD

OF MANUFACTURING SEMICONDUCTOR

**DEVICE** 

Examiner

Karla A. Moore

Group Art Unit

1763

August 7, 2003

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Applicant hereby petitions for a three-month extension, a petition pursuant to 37 C.F.R. §1.136(a) and authorization to charge the requisite fee being enclosed.

In the Office Action dated April 11, 2003, a Restriction Requirement divides the application into the following two Groups:

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Group I: claims 1, 2, 5 and 7, drawn to a semiconductor substrate stock/transfer vessel, (class 118, subclass 719); and

Group II: claims 10 - 12 and 15 - 17, drawn to a method of manufacturing a semiconductor devices (class 438).

According to the Restriction Requirement, Applicant elects Group I, comprising claims 1, 2, 5 and 7, without traverse. Applicant reserves his right to file a divisional application relating to the claims of Groups II at a later date.

Any fee due with this paper may be charged on Deposit Account 50-1290.

Respectfully submitted,

Thomas J. Bean Reg. No. 44,528

## **CUSTOMER NUMBER 026304**

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